

Docket No. 216692US2

IN RE APPLICATION OF: Takashi YAMADA, et al

SERIAL NO: 09/995,594

FILED: November 29, 2001

FOR: SEMICONDUCTOR CHIP WHICH COMBINES BULK AND SOI REGIONS AND SEPARATES SAME WITH PLURAL ISOLATION REGIONS

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

Transmitted herewith is an amendment in the above-identified application.

- No additional fee is required
- Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.
- Additional documents filed herewith:

The Fee has been calculated as shown below:

CLAIMS	CLAIMS REMAINING		HIGHEST NUMBER PREVIOUSLY PAID	NO. EXTRA CLAIMS	RATE	CALCULATIONS
TOTAL	30	MINUS	31	0	x \$18 =	\$0.00
INDEPENDENT	9	MINUS	8	1	x \$86 =	\$86.00
<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS					+ \$290 =	\$0.00
TOTAL OF ABOVE CALCULATIONS						\$86.00
<input type="checkbox"/> Reduction by 50% for filing by Small Entity						\$0.00
<input type="checkbox"/> Recordation of Assignment					+ \$40 =	\$0.00
						TOTAL \$86.00

- A check in the amount of \$0.00 is attached.
- Credit card payment form is attached to cover the fees in the amount of \$86.00
- Please charge any additional Fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.
- If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

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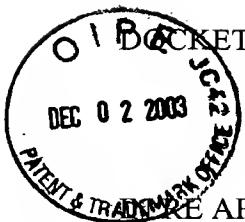
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"RESPONSE UNDER 37 CFR 1.116-
EXPEDITED PROCEDURE EXAMINING
GROUP 8826"



IN THE UNITED STATES PATENT & TRADEMARK OFFICE

THE APPLICATION OF

Takashi YAMADA, et al.

: EXAMINER: DICKEY, T.

SERIAL NO: 09/995,594

:

FILED: NOVEMBER 29, 2001

: GROUP ART UNIT: 2826

FOR: SEMICONDUCTOR CHIP WHICH
COMBINES BULK AND SOI REGIONS
AND SEPARATES SAME WITH PLURAL
ISOLATION REGIONS

AMENDMENT

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Office Action dated September 2, 2003, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 12 of this paper.